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Lecture 1: Introduction

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Arch. - Lecture 2b:

Mysteries in Comp.

Arch. (ETH Z ü rich,

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Architecture - Lecture 4:

Combinational Logic I

(ETH Zürich, Spring

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How a CPU is made let's

talk about ETHZ

(Digital Logic

Design Chapter3 - Gate

Level Minimization

(FULL Intel: The Making

of a Chip with 22nm/3D

Transistors | Intel Divide

~~and Conquer~~

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Digital Design
Algorithms—
Architecture

Master ' s in
Architectural Design

Digital Design
Fundamentals Interview
with Onur Mutlu @
ISCA 2019 on
computing research
& education (after
Maurice Wilkes Award)

Introduction to Logic

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Gates Computer

Digital Design \u0026

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Lecture 2a: Course Goals

\u0026 Logistics (ETH

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~~CSC611 Lecture 0:~~

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~~ARCHITECTURE~~ Logic

Minimization \u0026amp;

Schematic From HDL

Stanford Seminar - New

Golden Age for

Computer Architecture

Digital Design \u0026amp;

Com. Arch. - Lecture 5:

Combinational Logic II

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Architecture

takes a unique and modern approach to digital design. Beginning with digital logic gates and progressing to the design of combinational and sequential circuits, Harris and Harris use these fundamental building blocks as the basis for what follows: the design of an actual MIPS processor.

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Digital Design

Digital Design and
Computer Architecture:
Harris, David ...

Description. Digital Design and Computer Architecture, Second Edition, takes a unique and modern approach to digital design, introducing the reader to the fundamentals of digital logic and then showing step by step how to build a MIPS

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Digital Design

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microprocessor in both Verilog and VHDL. This new edition combines an engaging and humorous writing style with an updated and hands-on approach to digital design.

Digital Design and
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is designed for courses that combine digital logic design with computer organization/architecture or that teach these subjects as a two-course sequence....

Digital Design and
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David Harris ...

Description Digital
Design and Computer
Architecture, Second

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Edition, takes a unique
and modern approach to
digital design,

introducing the reader to
the fundamentals of
digital logic and then
showing step by step how
to build a MIPS
microprocessor in both
Verilog and VHDL.

Digital Design and
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Digital Design

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Digital Design and

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ARM Edition covers the

fundamentals of digital

logic design and

reinforces logic concepts

through the design of an

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Combining an engaging and humorous writing style with an updated and hands-on approach to digital design, this book takes the reader from the fundamentals of digital logic to the actual design of an ARM processor.

[Amazon.com: Digital Design and Computer Architecture: ARM ...](#)
Digital Design and

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Digital Design

Computer Architecture,
Second Edition, takes a
unique and modern
approach to digital
design, introducing the
reader to the
fundamentals of digital
logic and then showing
step by step how to build
a MIPS microprocessor
in both Verilog and
VHDL.

Digital Design and

Page 17/81

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Digital Design

Computer Architecture -

Computer ...

Digital Design and

Computer Architecture:

ARM® Edition. I/O

Systemse9. 9.1

INTRODUCTION.

Input/Output (I/O)

systems are used to

connect a computer with

external. devices called

peripherals. In a personal

computer, the devices

typically. include

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Digital Design

And Computer
Architecture
keyboards, monitors,
printers, and wireless
networks.

Digital Design and
Computer Architecture:
ARM® Edition

Digital Design and
Computer Architecture:
ARM® Edition © 2015
Chapter 7 <17> LDR Rd,
[Rn, imm12] STEP 5:
Read data from memory
and write it back to

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register file ... Computer
Architecture

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Computer Architecture:
ARM® Edition

CHAPTER 1 David
Money Harris and Sarah
L. Harris, Digital Design
and Computer
Architecture, Second
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Computer Architecture,
Second Edition, takes a
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approach to digital

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design, introducing the reader to the fundamentals of digital logic and then showing step by...

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Computer Architecture:
Edition 2 by ...

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Computer Architecture:
ARM Edition covers the
fundamentals of digital
logic design and

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reinforces logic concepts through the design of an ARM microprocessor.

Combining an engaging and humorous writing style with an updated and hands-on approach to digital design, this book takes the reader from the fundamentals of digital logic to the actual design of an ARM processor.

Digital Design and

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unique and modern
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design, introducing the
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fundamentals of digital
logic and then showing
step by step how to build
a MIPS microprocessor
in both Verilog and

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VHDL. Computer
Architecture

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Computer Architecture
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approach to digital
design, introducing the
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fundamentals of digital
logic and then showing

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step by step how to build a MIPS microprocessor in both Verilog and VHDL. This new edition combines an engaging and humorous writing style with an updated and hands-on approach to digital design.

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Computer Architecture
is designed for courses
that combine digital logic
design with computer
organization/architecture
or that teach these
subjects as a two-course
sequence.

Digital Design and
Computer Architecture
by David Money Harris
Academic courses
relevant to the project:

Page 28/81

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Digital Design

Digital Design,
Computer Architecture,
Analog Electronic, C

Programming Name:

RAM KRISHNA(2015A
3PS0280P) 314 Student

Write-up Short

Summary of work done
during PS-II : This report
discusses the various
tasks CPU verification
team works on.

Academic courses

Page 29/81

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relevant to the project

Digital Design ...

The TAD Content

division designs,

produces and supports

interactive digital content

experiences for lobbies,

innovation centers,

digital signage

applications and urban

spaces. The team is

focused on user-first

design thinking and

leveraging new

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And Computer
Architecture
technologies resulting in
seamless digital/physical
experiences.

Digital Design and
Computer Architecture:
ARM Edition covers the
fundamentals of digital
logic design and
reinforces logic concepts
through the design of an
ARM microprocessor.

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Combining an engaging and humorous writing style with an updated and hands-on approach to digital design, this book takes the reader from the fundamentals of digital logic to the actual design of an ARM processor. By the end of this book, readers will be able to build their own microprocessor and will have a top-to-bottom

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Digital Design

And Computer Architecture
understanding of how it works. Beginning with digital logic gates and progressing to the design of combinational and sequential circuits, this book uses these fundamental building blocks as the basis for designing an ARM processor. SystemVerilog and VHDL are integrated throughout the text in examples illustrating the

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Digital Design

And Computer Architecture
methods and techniques for CAD-based circuit design. The companion website includes a chapter on I/O systems with practical examples that show how to use the Raspberry Pi computer to communicate with peripheral devices such as LCDs, Bluetooth radios, and motors. This book will be a valuable resource for students

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Digital Design

And Computer
Architecture
taking a course that
combines digital logic
and computer

architecture or students

taking a two-quarter

sequence in digital logic

and computer

organization/architecture

. Covers the

fundamentals of digital

logic design and

reinforces logic concepts

through the design of an

ARM microprocessor.

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And Computer Architecture
Features side-by-side examples of the two most prominent Hardware Description Languages (HDLs)—SystemVerilog and VHDL—which illustrate and compare the ways each can be used in the design of digital systems. Includes examples throughout the text that enhance the reader's understanding and retention of key

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And Computer Architecture

concepts and techniques. The Companion website includes a chapter on I/O systems with practical examples that show how to use the Raspberry Pi computer to communicate with peripheral devices such as LCDs, Bluetooth radios, and motors. The Companion website also includes appendices covering practical digital

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Digital Design

And Computer Architecture
design issues and C
programming as well as
links to CAD tools,
lecture slides, laboratory
projects, and solutions to
exercises.

Digital Design and
Computer Architecture
Second Edition David
Money Harris and Sarah
L. Harris "Harris and
Harris have taken the
popular pedagogy from

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Digital Design

Computer Organization
and Design down to the
next level of refinement,
showing in detail how to
build a MIPS
microprocessor in both
Verilog and VHDL.

Given the exciting
opportunity that students
have to run large digital
designs on modern
FGPAs, the approach the
authors take in this book
is both informative and

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enlightening." -David A. Patterson, University of California at Berkeley, Co-author of Computer Organization and Design Digital Design and Computer Architecture takes a unique and modern approach to digital design. Beginning with digital logic gates and progressing to the design of combinational and sequential circuits,

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Digital Design

Harris and Harris use these fundamental building blocks as the basis for what follows: the design of an actual MIPS processor. SystemVerilog and VHDL are integrated throughout the text in examples illustrating the methods and techniques for CAD-based circuit design. By the end of this book, readers will be able to build their own

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microprocessor and will have a top-to-bottom understanding of how it works. Harris and Harris have combined an engaging and humorous writing style with an updated and hands-on approach to digital design. This second edition has been updated with new content on I/O systems in the context of general purpose

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Digital Design

And Computer

Architecture

processors found in a PC as well as microcontrollers found almost everywhere. The new edition provides practical examples of how to interface with peripherals using RS232, SPI, motor control, interrupts, wireless, and analog-to-digital conversion. High-level descriptions of I/O interfaces found in PCs

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include USB, SDRAM, WiFi, PCI Express, and others. In addition to

expanded and updated material throughout,

SystemVerilog is now featured in the

programming and code examples (replacing

Verilog), alongside

VHDL. This new edition also provides additional

exercises and a new appendix on C

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Architecture
programming to
strengthen the
connection between

programming and
processor architecture.

SECOND Edition

Features Covers the
fundamentals of digital
logic design and
reinforces logic concepts
through the design of a
MIPS microprocessor.

Features side-by-side
examples of the two most

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prominent Hardware Description Languages (HDLs)-System Verilog and VHDL-which illustrate and compare the ways each can be used in the design of digital systems. Includes examples throughout the text that enhance the reader's understanding and retention of key concepts and techniques. Companion Web site

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includes links to CAD
tools for FPGA design
from Altera and Mentor
Graphics, lecture slides,
laboratory projects, and
solutions to exercises.

David Money Harris
Professor of Engineering,
Harvey Mudd College

Sarah L. Harris Associate
Professor of Engineering,
Harvey Mudd College

Updated based on
instructor feedback with

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And Computer
Architecture
more exercises and new
examples of parallel and
advanced architectures,
practical I/O

applications, embedded
systems, and
heterogeneous

computing Presents
digital system design
examples in both VHDL
and SystemVerilog
(updated for the second
edition from Verilog),
shown side-by-side to

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compare and contrast
their strengths Includes a
new chapter on C

programming to provide
necessary prerequisites
and strengthen the
connection between
programming and
processor architecture

Companion Web site
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examples and ex

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approach to digital
design, introducing the
reader to the

fundamentals of digital
logic and then showing
step by step how to build
a MIPS microprocessor
in both Verilog and
VHDL. This new edition
combines an engaging
and humorous writing
style with an updated and
hands-on approach to
digital design. It presents

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Architecture
new content on I/O
systems in the context of
general purpose

processors found in a PC
as well as

microcontrollers found
almost everywhere.

Beginning with digital
logic gates and
progressing to the design
of combinational and
sequential circuits, the
book uses these
fundamental building

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And Computer Architecture

blocks as the basis for the design of an actual MIPS processor. It provides practical examples of how to interface with peripherals using RS232, SPI, motor control, interrupts, wireless, and analog-to-digital conversion.

SystemVerilog and VHDL are integrated throughout the text in examples illustrating the

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methods and techniques
for CAD-based circuit
design. There are also
additional exercises and
new examples of parallel
and advanced
architectures, practical
I/O applications,
embedded systems, and
heterogeneous
computing, plus a new
appendix on C
programming to
strengthen the

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Architecture
connection between
programming and
processor architecture.

This new edition will
appeal to professional
computer engineers and
to students taking a
course that combines
digital logic and
computer architecture.

Updated based on
instructor feedback with
more exercises and new
examples of parallel and

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Advanced Architectures,
Practical I/O

Applications, Embedded
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Examples in Both VHDL

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Compare and Contrast

Their Strengths Includes a

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new chapter on C
programming to provide
necessary prerequisites
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all exercises (PDF) Lab

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HDL for textbook

examples and exercise

solutions Lecture slides

(PPT) Sample exams\

Sample course syllabus

Figures from the text

(JPG, PPT)

This textbook covers

Page 58/81

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digital design,
fundamentals of
computer architecture,
and assembly language.

The book starts by
introducing basic
number systems,
character coding, basic
knowledge in digital
design, and components
of a computer. The book
goes on to discuss
information
representation in

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And Computer
Architecture
computing; Boolean
algebra and logic gates;
sequential logic;

input/output; and CPU
performance. The author
also covers ARM
architecture, ARM
instructions and ARM
assembly language which
is used in a variety of
devices such as cell
phones, digital TV,
automobiles, routers, and
switches. The book

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contains a set of laboratory experiments related to digital design using Logisim software; in addition, each chapter features objectives, summaries, key terms, review questions and problems. The book is targeted to students majoring Computer Science, Information System and IT and follows the ACM/IEEE

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2013 guidelines. •

Comprehensive textbook covering digital design, computer architecture,

and ARM architecture and assembly • Covers basic number system and coding, basic knowledge in digital design, and components of a computer • Features laboratory exercises in addition to objectives, summaries, key terms,

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review questions, and
problems in each chapter

Architecture

A COMPREHENSIVE
GUIDE TO THE
DESIGN &
ORGANIZATION OF
MODERN
COMPUTING
SYSTEMS Digital Logic
Design and Computer
Organization with
Computer Architecture
for Security provides

Page 63/81

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Architecture

practicing engineers and

students with a clear

understanding of

computer hardware

technologies. The

fundamentals of digital

logic design as well as the

use of the Verilog

hardware description

language are discussed.

The book covers

computer organization

and architecture, modern

design concepts, and

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Digital Design

And Computer

computer security
through hardware.

Architecture
Techniques for designing

both small and large

combinational and

sequential circuits are

thoroughly explained.

This detailed reference

addresses memory

technologies, CPU

design and techniques to

increase performance,

microcomputer

architecture, including

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And Computer Architecture
"plug and play" device interface, and memory hierarchy. A chapter on

security engineering methodology as it applies to computer architecture concludes the book.

Sample problems, design examples, and detailed diagrams are provided throughout this practical resource. **COVERAGE INCLUDES:**

Combinational circuits:

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Architecture

small designs
Combinational circuits:
large designs Sequential

circuits: core modules

Sequential circuits: small

designs Sequential

circuits: large designs

Memory Instruction set

architecture Computer

architecture:

interconnection Memory

system Computer

architecture: security

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Digital Design

Digital Design and Computer Organization introduces digital design as it applies to the creation of computer systems. It summarizes the tools of logic design and their mathematical basis, along with in depth coverage of combinational and sequential circuits. The book includes an accompanying CD that

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And Computer
Architecture
includes the majority of
circuits highlig

Digital Design and
Computer Organization
introduces digital design
as it applies to the
creation of computer
systems. It summarizes
the tools of logic design
and their mathematical
basis, along with in depth
coverage of
combinational and

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sequential circuits. The book includes an accompanying CD that includes the majority of circuits highlighted in the text, delivering you hands-on experience in the simulation and observation of circuit functionality. These circuits were designed and tested with a user-friendly Electronics Workbench package

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Digital Design

(Multisim Textbook
Edition) that enables
your progression from

truth tables onward to
more complex designs.

This volume differs from
traditional digital design
texts by providing a
complete design of an
AC-based CPU, allowing
you to apply digital
design directly to
computer architecture.

The book makes minimal

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reference to electrical properties and is vendor independent, allowing emphasis on the general design principles.

An introductory text to computer architecture, this comprehensive volume covers the concepts from logic gates to advanced computer architecture. It comes with a full spectrum of

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Architecture
exercises and web-
downloadable support
materials, including

assembler and simulator,
which can be used in the
context of different

courses. The authors also
make available a

hardware description,
which can be used in labs
and assignments, for
hands-on

experimentation with an
actual, simple processor.

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Architecture
This unique compendium is a useful reference for

undergraduates,

graduates and

professionals majoring in

computer engineering,

circuits and systems,

software engineering,

biomedical engineering

and aerospace

engineering.

The new RISC-V Edition

Page 74/81

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Organization and Design

features the RISC-V

open source instruction

set architecture, the first

open source architecture

designed to be used in

modern computing

environments such as

cloud computing, mobile

devices, and other

embedded systems. With

the post-PC era now

upon us, Computer

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moves forward to explore
this generational change
with examples, exercises,
and material highlighting
the emergence of mobile
computing and the
Cloud. Updated content
featuring tablet
computers, Cloud
infrastructure, and the
x86 (cloud computing)
and ARM (mobile
computing devices)

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An online companion

Web site provides

advanced content for

further study,

appendices, glossary,

references, and

recommended reading.

Features RISC-V, the

first such architecture

designed to be used in

modern computing

environments, such as

cloud computing, mobile

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devices, and other
embedded systems
Includes relevant

examples, exercises, and
material highlighting the
emergence of mobile
computing and the cloud

Computing the

Environment presents
practical workflows and
guidance for designers to
get feedback on their
design using digital

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design tools on environmental performance. Starting with an extensive state-of-the-art survey of what top international offices are currently using in their design projects, this book presents detailed descriptions of the tools, algorithms, and workflows used and discusses the theories that underlie these methods.

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Project examples from

Transsolar

Klimaengineering, Buro

Happold's SMART

Group, Behnish

Behnisch Architects,

Thomas Herzog,

Autodesk Research are

contextualized with

quotes and references to

key thinkers in this field

such as Eric Winsberg,

Andrew Marsh, Michelle

Addington and Ali

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